RAPITA SYSTEMS
A DANLAW COMPANY

Safety through quality

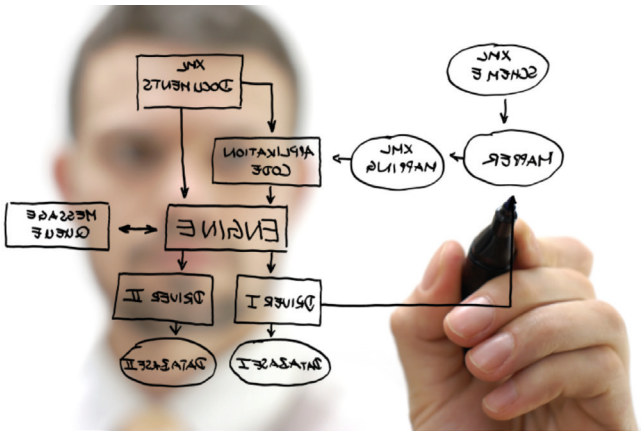
PRODUCT BRIEF

R**VS** Proof of Concept Study

Product brief: R**VS** Proof of Concept Study

When you believe that R**VS** could benefit your development, but need to know more about integrating it with your specific system, a Proof of Concept (PoC) Study is the logical next step.

Because of the variety and complexity of embedded systems, our experience tells us that a one-size-fits-all approach to on-target verification tools is impractical. This is why our PoC studies focus on understanding your specific development needs and environment to develop an approach and R**VS** integration that extracts the best possible verification results from your system.



Key principles

Three key principles lie at the heart of every Rapita PoC study; PoCs:

1. Demonstrate the capabilities of the R**VS** tools you need
2. Prove the feasibility and potential of using R**VS** on your system
3. Offer a platform for further collaboration

During PoC studies, selected R**VS** tools are tested on a subset of your system, results are analyzed and a report is produced to demonstrate the benefits you could achieve by using R**VS**.

Continued collaboration is a key outcome of PoC studies, so next steps are identified and discussed during every study.

How do Proof of Concept studies work?

Step 1 – Agree objectives

We agree on a subset of your system that is suitable for preliminary analysis with R**VS** and gather information from you on the technical details of your system (processor, memory, compiler, etc) using our *Target Integration Questionnaire*.

Based on this information, we agree on objectives, milestones and timescales for the PoC study.

Step 2 – Preparatory research

We learn more about your system, needs and ideal outcomes of the study through technical discussions with your engineers and perform preparation work ahead of working on the PoC either remotely or on-site at your facility.

Step 3 – Implementation

Our Field Application Engineering team works with you to integrate R**VS** into and produce R**VS** results for the agreed subset of your system. This may be carried out remotely or, in some cases, through a site visit (travel costs may apply).

Step 4 – Present results

We gather evidence and data from the study, which we use to produce the final PoC report. This includes:

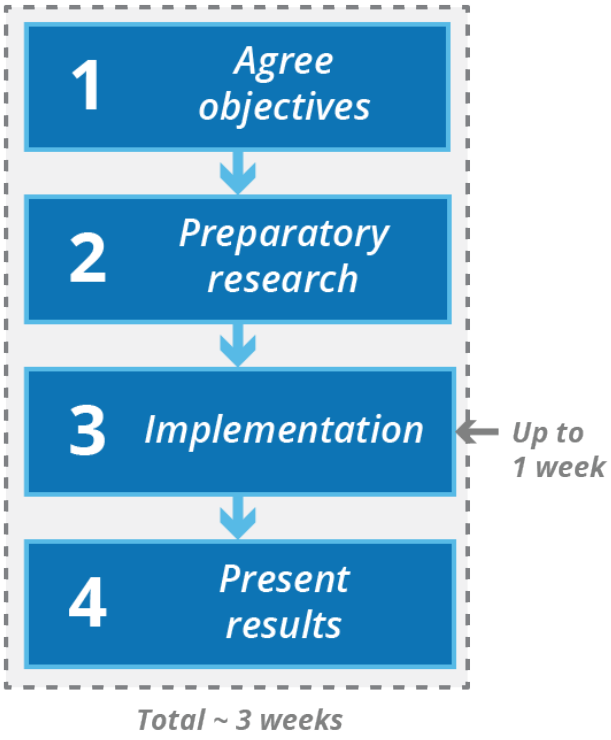
- An executive summary
- A summary of your needs
- A technical description of your system
- A list of the activities we performed throughout the PoC study
- Results and recommended next steps

We then deliver this report, gather your feedback on the study, and agree on a plan to move forward.

Timescales

We aim to complete the full PoC study (Steps 1 - 4) in a time agreed with you, typically around three weeks.

The *Implementation* (Step 3) lasts for up to one week.



RVS Proof of Concept Study timeline

What can I expect from a PoC study?

R**VS** Proof of Concept studies provide the following:

- Evidence of how R**VS** can be integrated into your development environment
- Evidence of how R**VS** can generate results on your system
- Early identification of any risks to the integration
- Preliminary integration work that can be re-used during full R**VS** integrations
- A PoC report describing the work that was carried out, results and outcomes (see *PoC report contents*)

PoC report contents

- Executive Summary – describes the work carried out, when it was conducted, and key outcomes
- Customer Needs – establishes the key needs that you hoped to be addressed by the study
- Technical description of your system – detailed description of the software and hardware you are using
- Activities undertaken – describes the activities we carried out during the study, objectives and achievements
- Results – lists and describes results achieved from R**VS** analysis of the agreed subset of your system
- Conclusion – an overview of the study and recommended next steps
- Appendix – technical details of the integration that was engineered

Next steps

If you want us to conduct a PoC study with you or want to learn more, please contact us at info@rapitasystems.com.

We can provide example reports demonstrating the kind of results you can expect to achieve during a PoC study at your request.

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About Rapita

Rapita Systems provides on-target software verification tools and services globally to the embedded aerospace and automotive electronics industries.

Our solutions help to increase software quality, deliver evidence to meet safety and certification objectives and reduce costs.

Find out more

A range of free high-quality materials are available at:
rapitasystems.com/downloads

SUPPORTING CUSTOMERS WITH:

Tools	Engineering Services	Multicore verification
Rapita **Verification Suite**:	V&V Services	**MACH**[178]
Rapi**Test**	Integration Services	Multicore Timing Solution
Rapi**Cover**	Qualification	
Rapi**Time**	SW/HW Engineering	
Rapi**Task**	Compiler Verification	

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